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000530 7590 08/31/2004
LERNER, DAVID, LITTENBERG,
KRUMHOLZ & MENTLIK
600 SOUTH AVENUE WEST
WESTFIELD, NJ 07090

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11/26/2004 HDEYENEZ 00000010 121095 -10618833

01 FC:1501 1370.00 DA
02 FC:1504 300.00 DA
03 FC:8001 39.00 DA

FILING DATE 07/11/2003
FIRST NAMED INVENTOR Peter Pochtmüller
ATTORNEY DOCKET NO. INTBCH 3.0-090
CONFIRMATION NO. 5265

TITLE OF INVENTION: STRUCTURE AND METHOD OF MULTIPLEXING BITLINE SIGNALS WITHIN A MEMORY ARRAY

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1,370	\$300	\$1,670	11/30/2004
EXAMINER	ART UNIT	CLASS-SUBCLASS			
LB, VU ANH	2824	365-149000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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(A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY)

Infineon Technologies AG Germany

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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Typed or printed name John R. Nelson

Date 11/23/04
Registration No. 26,573

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FACSIMILE TRANSMISSION

ISSUE FEE TRANSMITTAL
AND PUBLICATION FEE

ATTORNEY DOCKET NO.: INTECH 3.0-090

APPLICATION NO.: 10/618,333

CONFIRMATION NO.: 5265

MAILING DATE OF NOTICE OF ALLOWANCE: August 31, 2004

FAX NUMBER: (703) 746-4000

PAGES INCLUDING COVER SHEET: 2

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